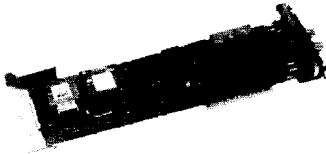# BUS-65531

MIL-STD-1553 BC/RT/MT
IBM PS/2 Microchannel® Interface Unit

FEATURES

- **PS/2 Microchannel Interface With Intelligent BC/RT/MT**

- **Full-Size MCA Card**

- **1553B Notice 2/STANAG 3838 Compliant**

- **Supports All 1553B Dual Redundant Message Formats And Mode Codes**

- **Dedicated 8K x 16 Dual Access Ram**

- **Double/Circular Buffer Memory Architecture To Insure Data Integrity**

DESCRIPTION

The BUS-65531 provides full, intelligent interfacing between the serial dual redundant MIL-STD-1553B Data Bus and the PS/2 Microchannel Bus. Software controls the BUS-65531's operation as either a 1553 Bus Controller (BC), Remote Terminal Unit (RT), or Bus Monitor (MT).

The BUS-65531 comes packaged on one full-size MCA printed circuit card. Its on-board 8K x 16 dual access RAM is double buffered, preventing partially updated data from being read by the CPU or transmitted to the 1553 Data Bus.

Additional features include a dynamic wrap-around Built-In-Test (BIT), on-board Time Tag Counter, software programmable interrupt level and RT address selection. The BUS-65531 supports all dual redundant mode codes and message formats. Its full compliance with MIL-STD-1553B makes it an excellent choice for dynamic simulation applications.
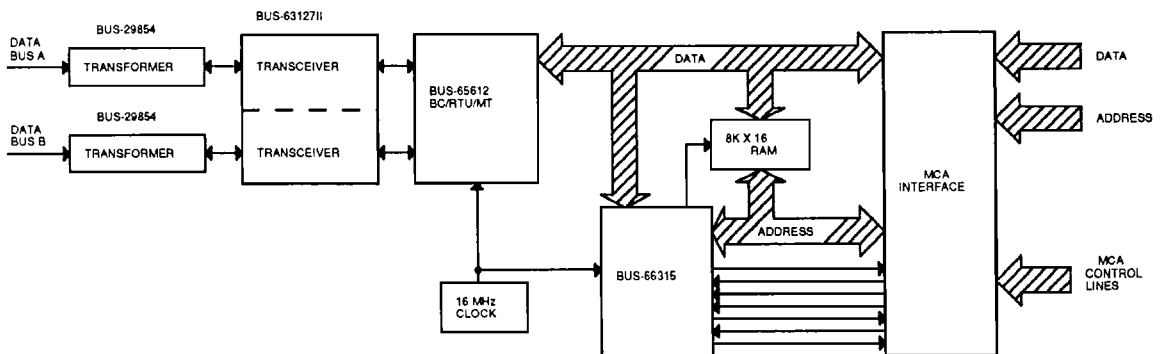

FIGURE 1. BUS-65531 BLOCK DIAGRAM

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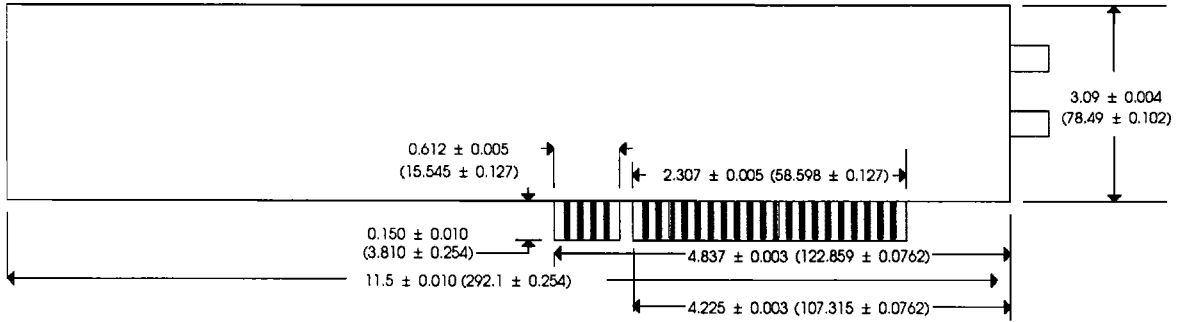
DDC
ILC DATA DEVICE
CORPORATION®

0.612 ± 0.005
(15.545 ± 0.127)

2.307 ± 0.005 (58.598 ± 0.127)

3.09 ± 0.004
(78.49 ± 0.102)

0.150 ± 0.010
(3.810 ± 0.254)

4.837 ± 0.003 (122.859 ± 0.0762)

11.5 ± 0.010 (292.1 ± 0.254)

4.225 ± 0.003 (107.315 ± 0.0762)

FIGURE 2. BUS-65531 MECHANICAL OUTLINE

ORDERING INFORMATION

BUS-65531

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